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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Present Application:

Applicants : Timothy B. Cowles, Brian M.
Shirley and Greg A. Blodgett

Attorney Docket No.: 501030.01

Filed : Concurrently herewith

Title : LOW POWER AUTO-REFRESH CIRCUIT AND METHOD FOR DYNAMIC
RANDOM ACCESS MEMORIESPRELIMINARY AMENDMENT

Box Patent Application
Commissioner of Patents
Washington, D.C. 20231

Sir:

Please amend the above-identified application as follows:

In the Claims:

Please amend claims 42-111 as follows:

41. The dynamic random access memory of claim 28 wherein the refresh decoder comprises:

a first decoder operable to decode the at least one internal command indicative of a refresh of the DRAM and to generate a predetermined refresh signal responsive thereto;

a timer coupled to the first decoder, the timer being triggered by the predetermined refresh signal and generating a refresh terminate signal a predetermined period after the predetermined refresh signal; and

a second decoder coupled to the first decoder and to the timer, the second decoder being operable to generate a refresh command and the first and second refresh signals responsive to the predetermined refresh signal, the second decoder further being operable to terminate the